



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Masahiro NAKANISHI et al.

Group Art Unit : Not Yet Assigned

Appl. No. : 10/598,307 (National Stage  
of PCT/JP2005/003136)

Examiner : Not Yet Assigned

I.A. Filed : February 25, 2005

Confirmation No.: 2576

For : SEMICONDUCTOR MEMORY DEVICE

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop AMENDMENT  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir :

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and §§1.97-1.98 and supplemental to the Information Disclosure Statement that was filed on November 24, 2006, Applicants hereby submit a copy of a Notification of Transmittal of Copies of Translation of the International Preliminary Report on Patentability (including Forms PCT/IB/338 and PCT/IPEA/409) with respect to International Patent Application No. PCT/JP2005/003136, of which the present application is the U.S. National Stage Application. The following documents were cited in the International Preliminary Report on Patentability:

- (1) Japanese Patent Publication No. JP 2001-5928 A; and
- (1) Japanese Patent Publication No. JP 2003-15929 A, and patent family member U.S. Patent Application Publication No. 2003/0189860 A1.